

REMARKS

Reconsideration of the above-identified application is respectfully requested.

Replacement sheets with figures labeled "prior art" are enclosed.

Headings are optional and applicant would prefer not to include headings.

The first paragraph on page 3 of the specification has been clarified.

The multiple dependency of claims 3–8, 11, and 13–19 has been eliminated.

The alternative expressions in claims 10 and 15 have been changed.

Claims 1, 2, 3, 4, 9, and 10 were rejected as anticipated by Shen. Applicant's FIG. 4 and FIG. 6 are reproduced, in part, below.

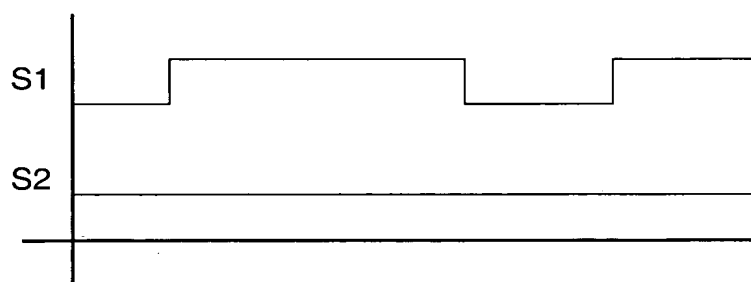


FIG. 4 (partial)

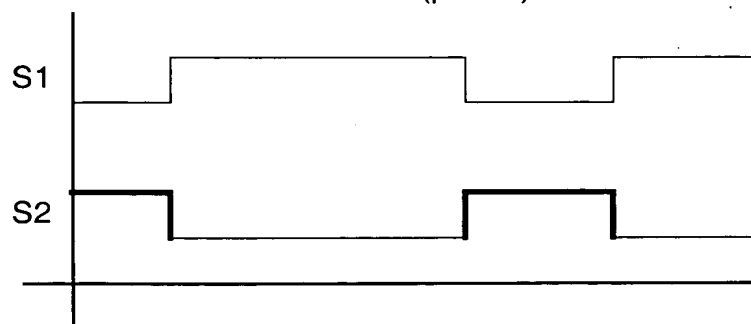
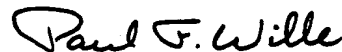


FIG. 6 (partial)

The last clause of claim 1 recites that the "two switches (61, 62) are always switched substantially simultaneously in counter-phase." This is illustrated in FIG. 6, emphasized above, and is not disclosed or suggested by the prior art. As disclosed in applicant's specification (page 8, line 32ff), by switching in counter-phase, current flows up through the channel of MOSFET 62 (FIG. 5) rather than through diode 64. None of this is disclosed or suggested by the Shen patent.

In view of the foregoing amendments and remarks, it is respectfully submitted that claims 1-19 are in condition for allowance and a Notice to that effect is respectfully requested.

Respectfully submitted,



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